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APPLICATION NO.	Fi	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
10/612,744	06/30/2003		Quat T. Vu	884.796US2	5516	
21186	7590	04/19/2004		EXAMINER		
SCHWEGN	IAN, LU	INDBERG, WOES	TRAN, MA	TRAN, MAI HUONG C		
P.O. BOX 29	938					
MINNEAPO	LIS, MN	55402	ART UNIT	PAPER NUMBER		
				2818		

DATE MAILED: 04/19/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	No.	Applicant(s)					
		10/612,744		VU ET AL.					
	Office Action Summary	Examiner		Art Unit					
		Mai-Huong	Tran	2818					
Period fo	Th MAILING DATE of this communication Reply	on appears on the d	over sheet v	vith the correspondence a	ddress				
A SH THE - Exter after - If the - If NO - Failu Any	ORTENED STATUTORY PERIOD FOR I MAILING DATE OF THIS COMMUNICAT asions of time may be available under the provisions of 37 SIX (6) MONTHS from the mailing date of this communical period for reply specified above is less than thirty (30) day period for reply is specified above, the maximum statutory are to reply within the set or extended period for reply will, be teply received by the Office later than three months after the patent term adjustment. See 37 CFR 1.704(b).	TION. CFR 1.136(a). In no evention. s, a reply within the statuto y period will apply and will a y statute, cause the applica	, however, may a ry minimum of th expire SIX (6) MO ation to become A	reply be timely filed rty (30) days will be considered time NTHS from the mailing date of this of BANDONED (35 U.S.C. § 133).					
Status									
1)⊠	Responsive to communication(s) filed or	n <u>30 June 2003</u> .							
·		This action is no	n-final.						
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.								
Disposition of Claims									
5)□									
Applicati	ion Papers								
10)□	The specification is objected to by the Ex The drawing(s) filed on is/are: a) Applicant may not request that any objection Replacement drawing sheet(s) including the The oath or declaration is objected to by	accepted or b) to the drawing(s) be correction is required	held in abeya if the drawing	nce. See 37 CFR 1.85(a). g(s) is objected to. See 37 C					
Priority ι	ınder 35 U.S.C. § 119								
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 									
Attachmen	t(s)								
	e of References Cited (PTO-892)			Summary (PTO-413)					
3) 🛛 Infori	e of Draftsperson's Patent Drawing Review (PTO-9 mation Disclosure Statement(s) (PTO-1449 or PTO/ r No(s)/Mail Date <u>6/30/03</u> .	(SB/08)		(s)/Mail Date Informal Patent Application (PT 	O-152)				

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DETAILED ACTION

Claim Rejections - 35 U.S.C. § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 9-11, and 16 are rejected under 35 U. S. C. § 102 (e) as being anticipated by U.S. Patent No. 6,162,661 to Link.

Regarding to claims 9-11 and 16, Link discloses a method of fabricating a microelectronic package, comprising providing a microelectronic package core having a first surface and an opposing second surface, the microelectronic package core having at least one opening 100 defined therein extending from the microelectronic package core first surface to microelectronic package core second surface; disposing at least one microelectronic die 58 within at least one opening, at least one microelectronic die having an active surface; and adhering microelectronic package core to at least one microelectronic die with an encapsulation material (col. 7, lines 1-66, col. 9, lines 36-50, and figs. 1, 5A, 10A).

Claim Rejections - 35 U.S.C. § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 12-15, 17-29 are rejected under 35 U.S.C. 103 (a) as being unpatentable over U.S. Patent No. 6,162,661 to Link in view of the remark.

Regarding to claims 12 and 17, Link discloses the claimed invention except for the method further including forming at least one second dielectric material layer disposed over at least one first conductive trace and at least one first dielectric material layer.

It would have been obvious to one of ordinary skill in the art at the time the invention was made to form at least one second dielectric material layer disposed over at least one first conductive trace and at least one first dielectric material layer.

Regarding to claims 13 and 18, Link discloses the claimed invention except for the method further including forming at least one second conductive trace to extend through and reside on at least one second dielectric material layer.

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It would have been obvious to one of ordinary skill in the art at the time the invention was made to form at least one second conductive trace to extend through and reside on at least one second dielectric material layer.

Regarding to claims 14, 19, 23, the method wherein the microelectronic package core comprises providing a microelectronic package core selected from the group consisting of bismaleimide triazine resin based material, an FR4 material, polyimides, ceramics, and metals (col. 1, line 26, col. 4, line 7).

Regarding to claims 15 and 20-22, 24-29, Link discloses a method of fabricating a microelectronic package comprising providing a mecroelectronic package core having a first surface and an opposing second surface, the microelectronic package core having at least one opening defined therein extending from the microelectronic package core first surface to the microelectronic package core second surface;

Link does not disclose abutting a protective film against the microelectronic package core first surface, wherein an active surface of microelectronic die abuts a portion of the protective film; adhering the microelectronic package core to at least one microelectronic die with an encapsulation material, wherein a portion of the encapsulation material fills a portion of the opening to form at least one encapsulation material surface abutting the protective film; and removing the protective film.

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It would have been obvious to abut a protective film against the microelectronic package core first surface, wherein an active surface of microelectronic die abuts a portion of the protective film; adhere the microelectronic package core to at least one microelectronic die with an encapsulation material, wherein a portion of the encapsulation material fills a portion of the opening to form at least one encapsulation material surface abutting the protective film; and remove the protective film.

Conclusion

Any inquiry concerning this communication on earlier communications from the examiner should be directed to Mai-Huong Tran, (571) 272-1796. The examiner can normally be reached on Monday-Thursday from 8:00 AM to 6:30 PM. The examiner's supervisor, David Nelms can be reached on (571) 272-1787.

The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Mai-Huong Tran

Supervisory Patent Examiner
Technology Center 2800

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